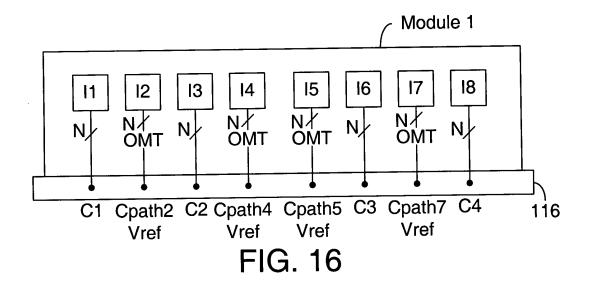


FIG. 15



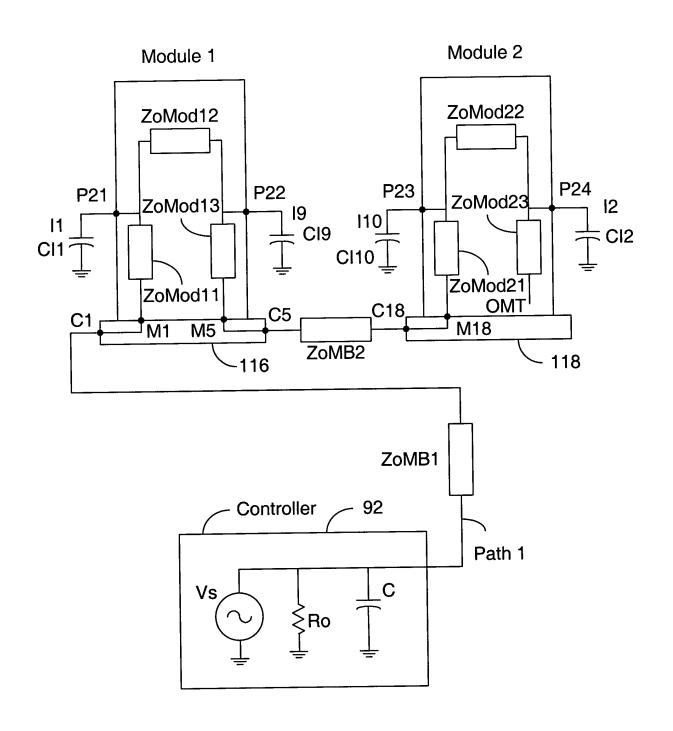


FIG. 17

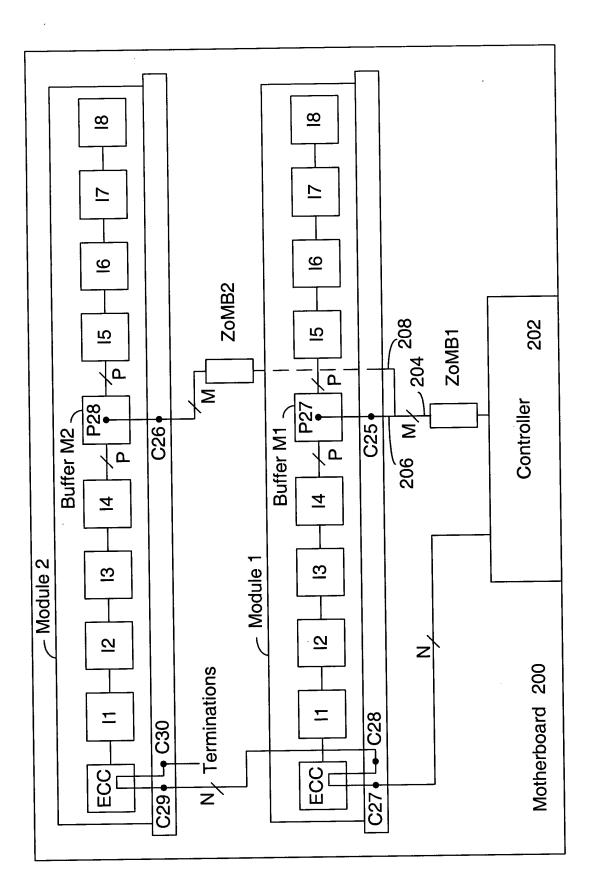


FIG. 18

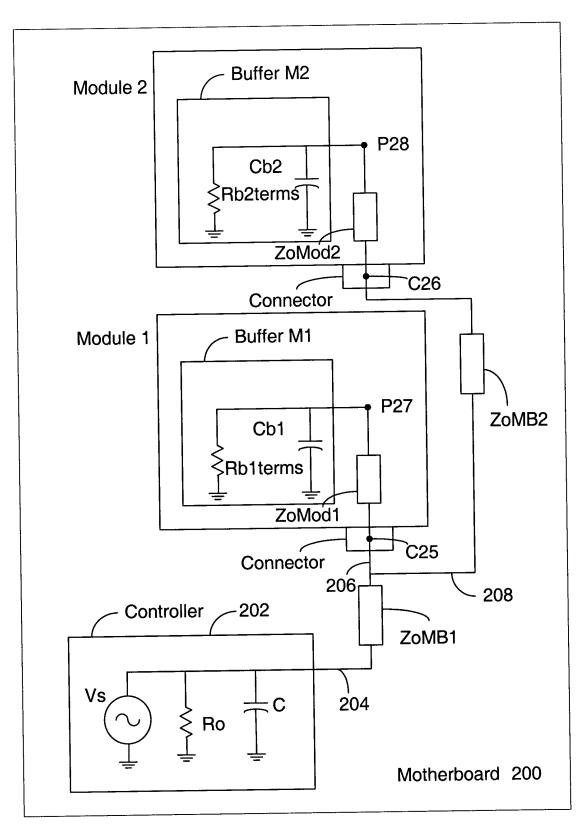


FIG. 19

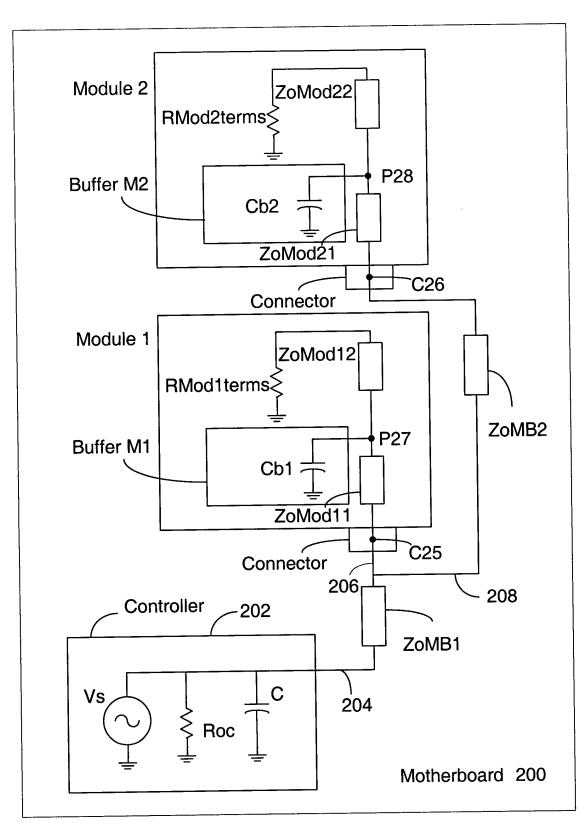


FIG. 20

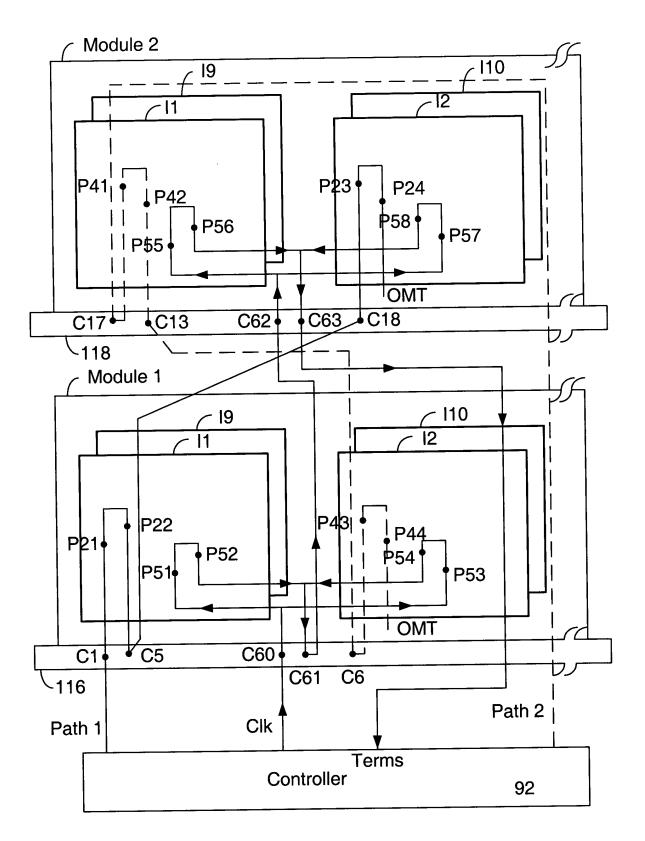


FIG. 21

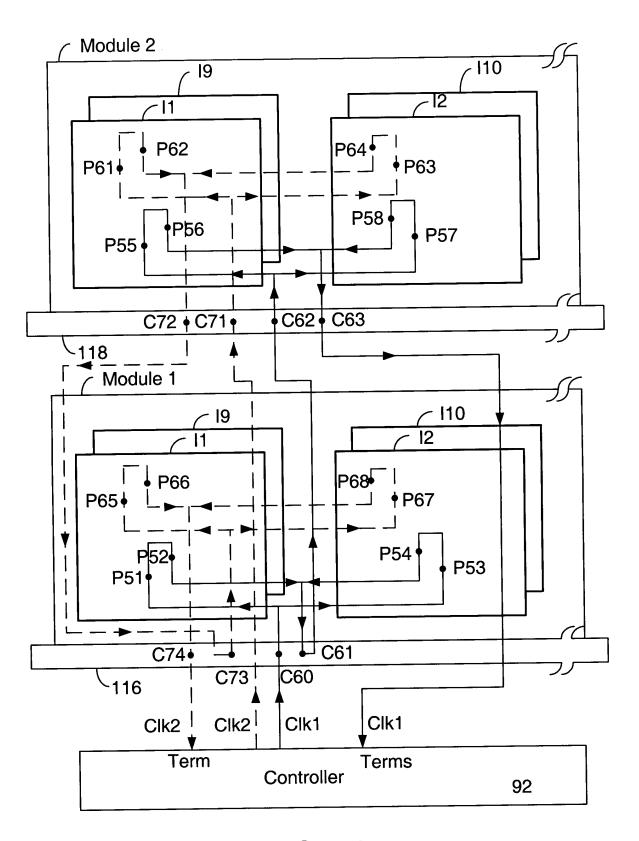
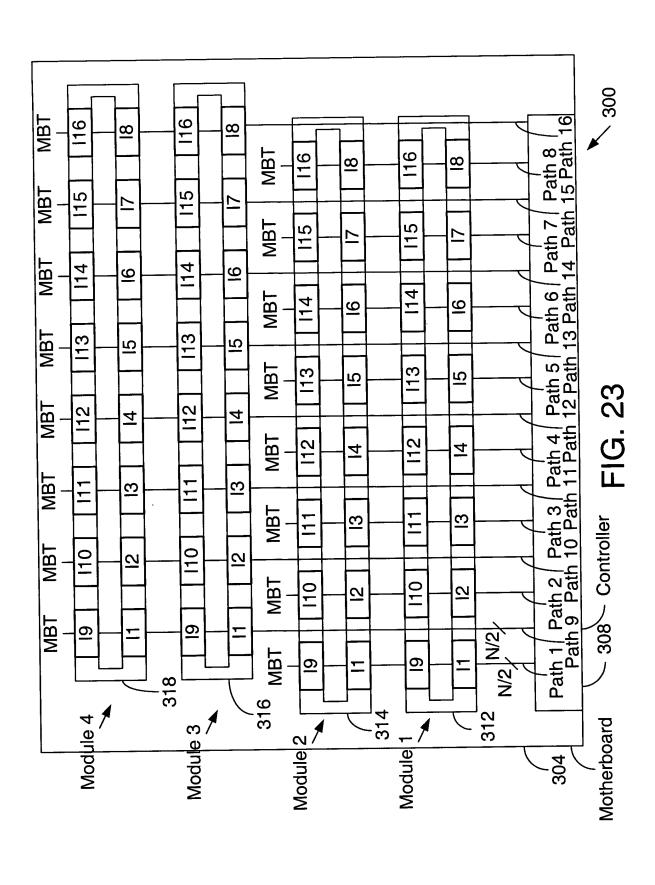
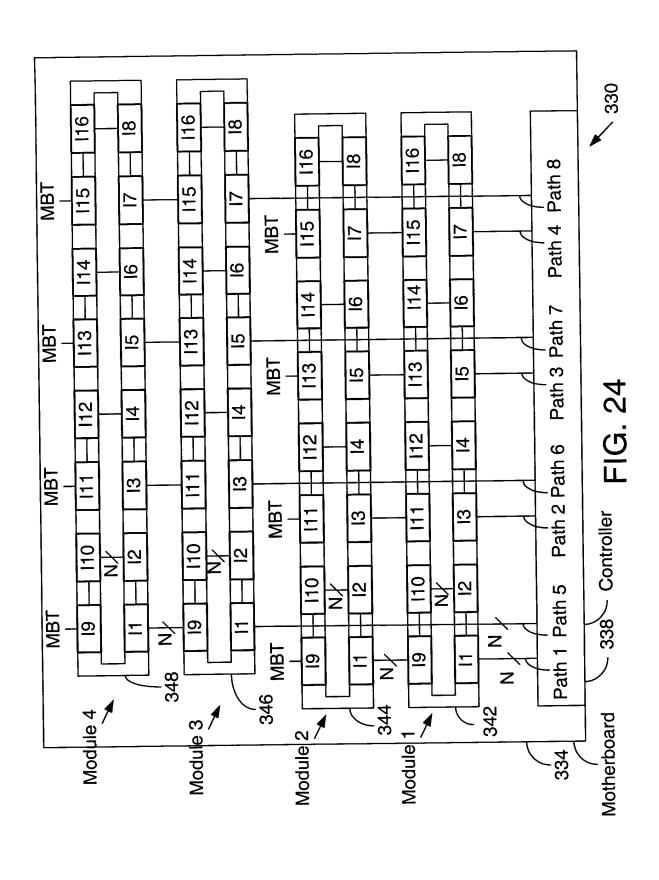


FIG. 22





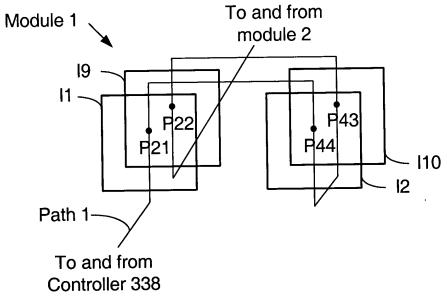


FIG. 25

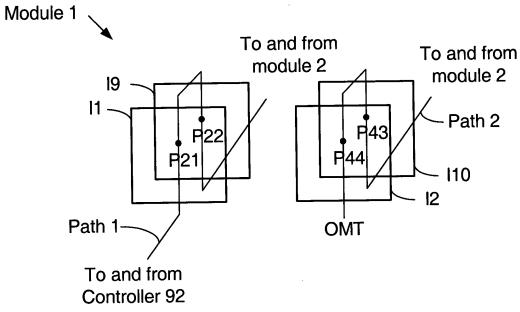


FIG. 26

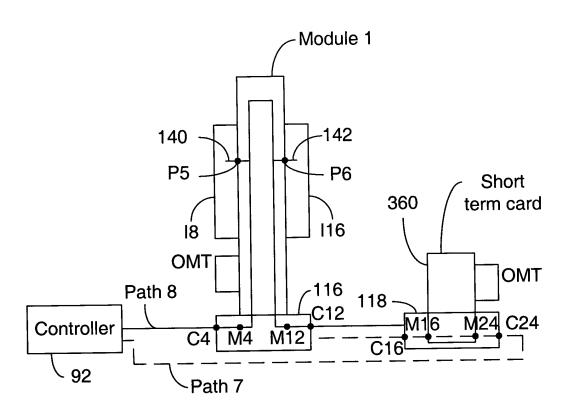


FIG. 27

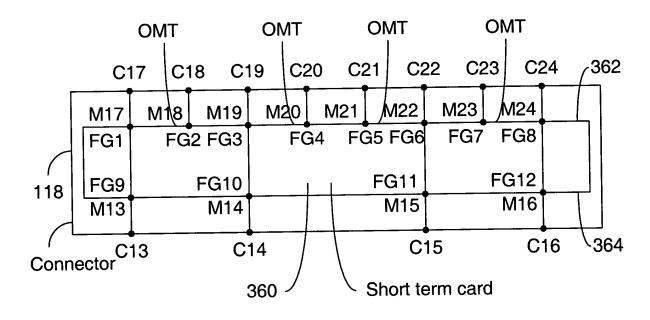


FIG. 28

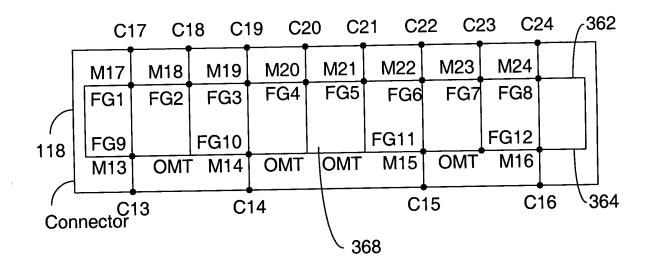


FIG. 29

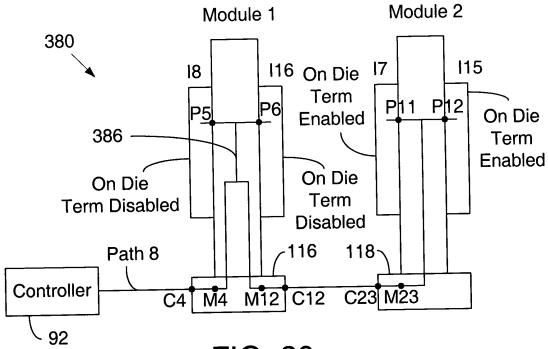


FIG. 30

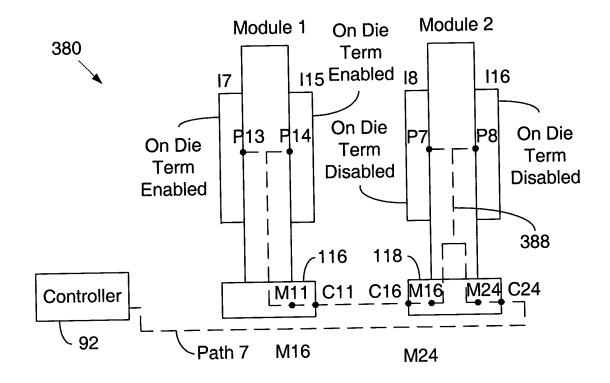


FIG. 31

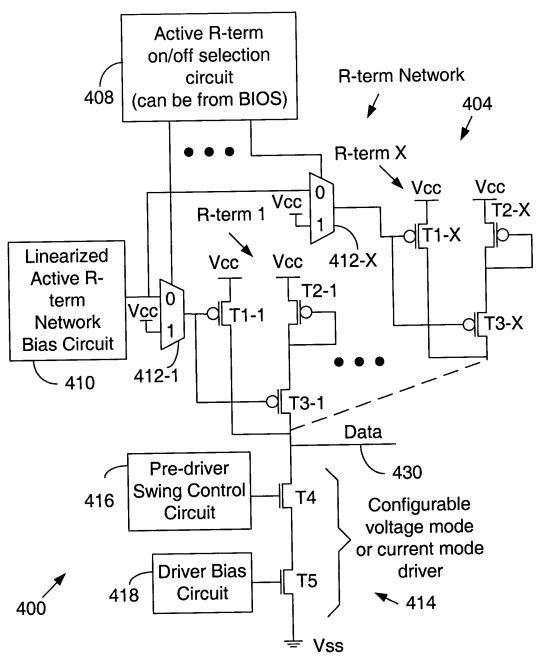


FIG. 32

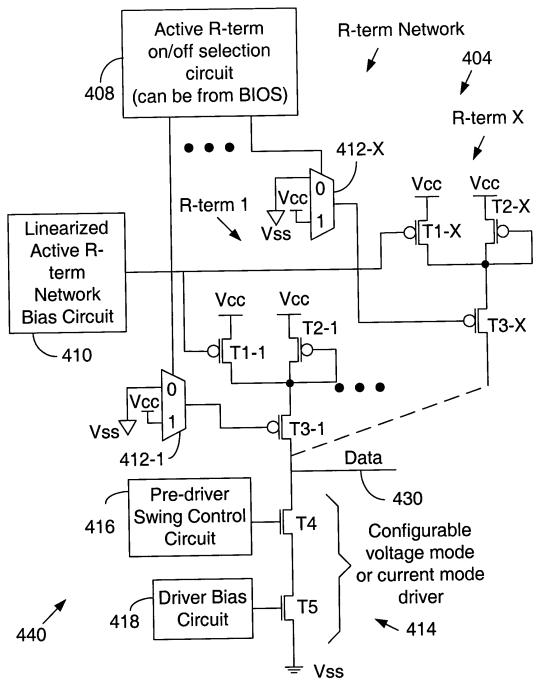
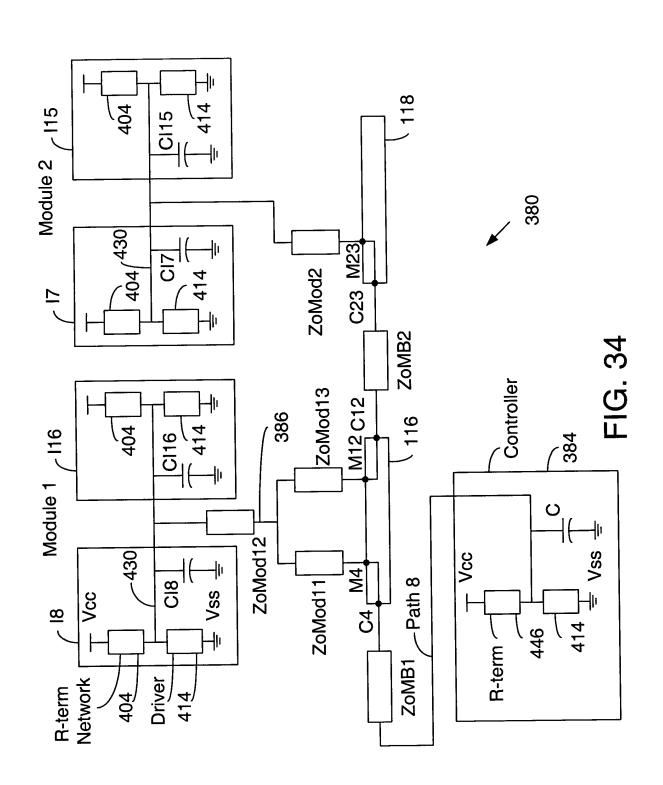
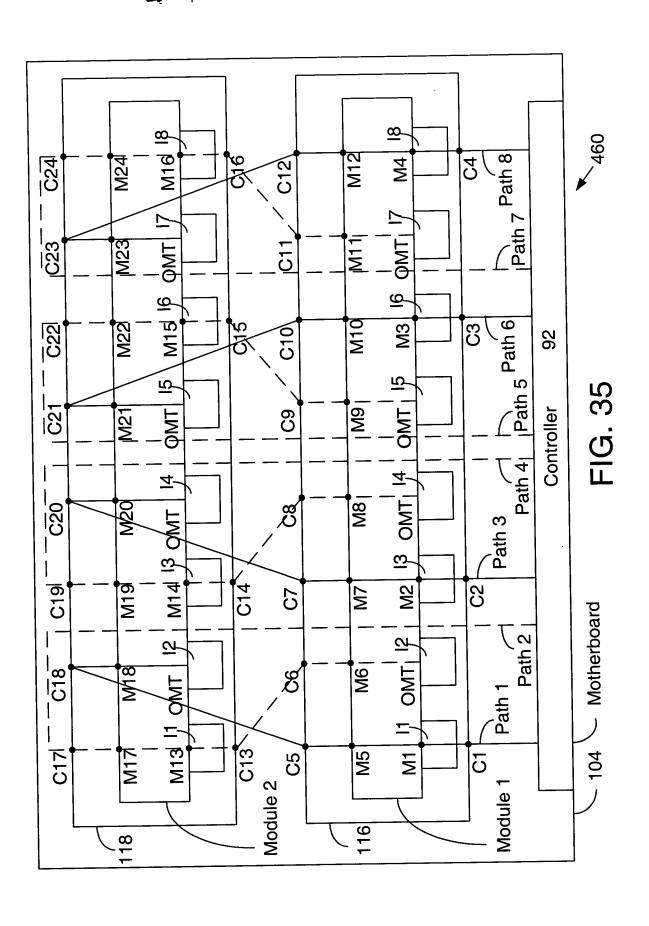
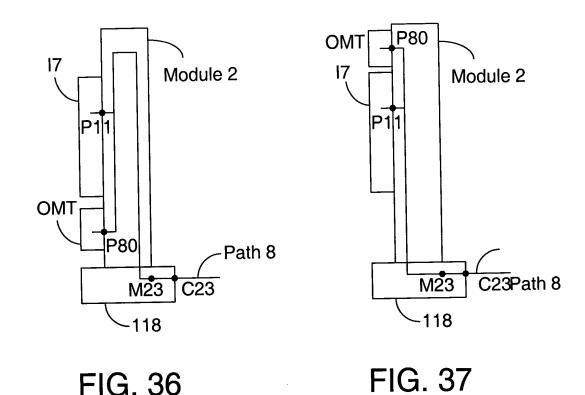


FIG. 33







Module 2 Module 3 Module 1 Path 3 Path 1 Path 5 Path 7 Path 6 Path 8 Path 4 Path 2 - 482 Controller 480 FIG. 38

FIG. 36

